Fig. 1

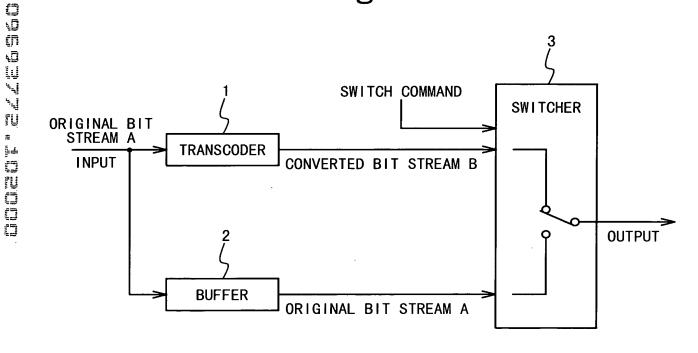


Fig. 2A

INPUTTED ORIGINAL BIT STREAM A

ig. 2B

CONVERTED BIT STREAM B OUTPUTTED BY TRANSCODER 1

Fig. 26

ORIGINAL BIT STREAM A OUTPUTTED BY BUFFER 2

		:		•
•		B3		A3
A3		B2		A2
A2		81		A1
A1	tp		tp A	
A		- B		8 A 2

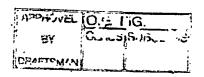


Fig. 3

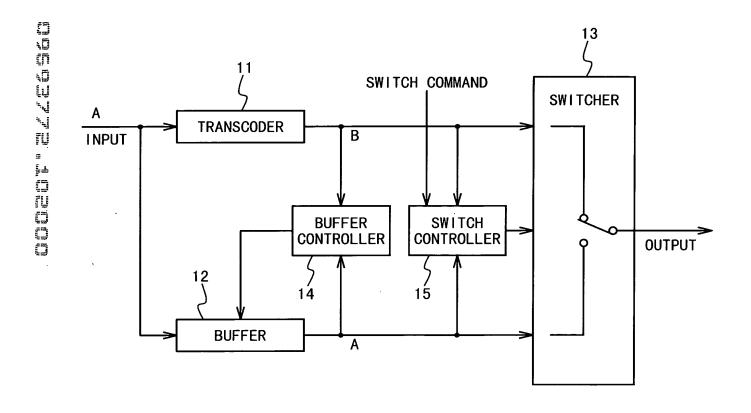


Fig. 4A

INPUTTED ORIGINAL BIT STREAM A

Fig. 4B

CONVERTED BIT STREAM B OUTPUTTED BY TRANSCODER 11

Fig. 4C

BUFFER CONTROLLER 14

i g. 4D

ORIGINAL BIT STREAM A OUTPUTTED BY BUFFER 12

		•••	_		•
•		B3			A3
A3		B2	_	_	A2
A2		B1	_	_	A1
A1	tp		_	_	
		~ _	_	_	<u> </u>

